Device Modeling Report

COMPONENTS: Power MOSFET (Professional)
PART NUMBER: IRFIB7N50A
MANUFACTURER: International Rectifier
REMARK: Body Diode (Standard)

Bee Technologies Inc.

POWER MOSFET MODEL

Pspice model parameter	Model description
LEVEL	
L	Channel Length
W	Channel Width
KP	Transconductance
RS	Source Ohmic Resistance
RD	Ohmic Drain Resistance
VTO	Zero-bias Threshold Voltage
RDS	Drain-Source Shunt Resistance
TOX	Gate Oxide Thickness
CGSO	Zero-bias Gate-Source Capacitance
CGDO	Zero-bias Gate-Drain Capacitance
CBD	Zero-bias Bulk-Drain Junction Capacitance
MJ	Bulk Junction Grading Coefficient
PB	Bulk Junction Potential
FC	Bulk Junction Forward-bias Capacitance Coefficient
RG	Gate Ohmic Resistance
IS	Bulk Junction Saturation Current
N	Bulk Junction Emission Coefficient
RB	Bulk Series Resistance
PHI	Surface Inversion Potential
GAMMA	Body-effect Parameter
DELTA	Width effect on Threshold Voltage
ETA	Static Feedback on Threshold Voltage
THETA	Modility Modulation
KAPPA	Saturation Field Factor
VMAX	Maximum Drift Velocity of Carriers
XJ	Metallurgical Junction Depth
UO	Surface Mobility

Body Diode Model

Pspice model parameter	Model description
IS	Saturation Current
N	Emission Coefficient
RS	Series Resistance
IKF	High-injection Knee Current
CJO	Zero-bias Junction Capacitance
M	Junction Grading Coefficient
VJ	Junction Potential
ISR	Recombination Current Saturation Value
BV	Reverse Breakdown Voltage(a positive value)
IBV	Reverse Breakdown Current(a positive value)
TT	Transit Time

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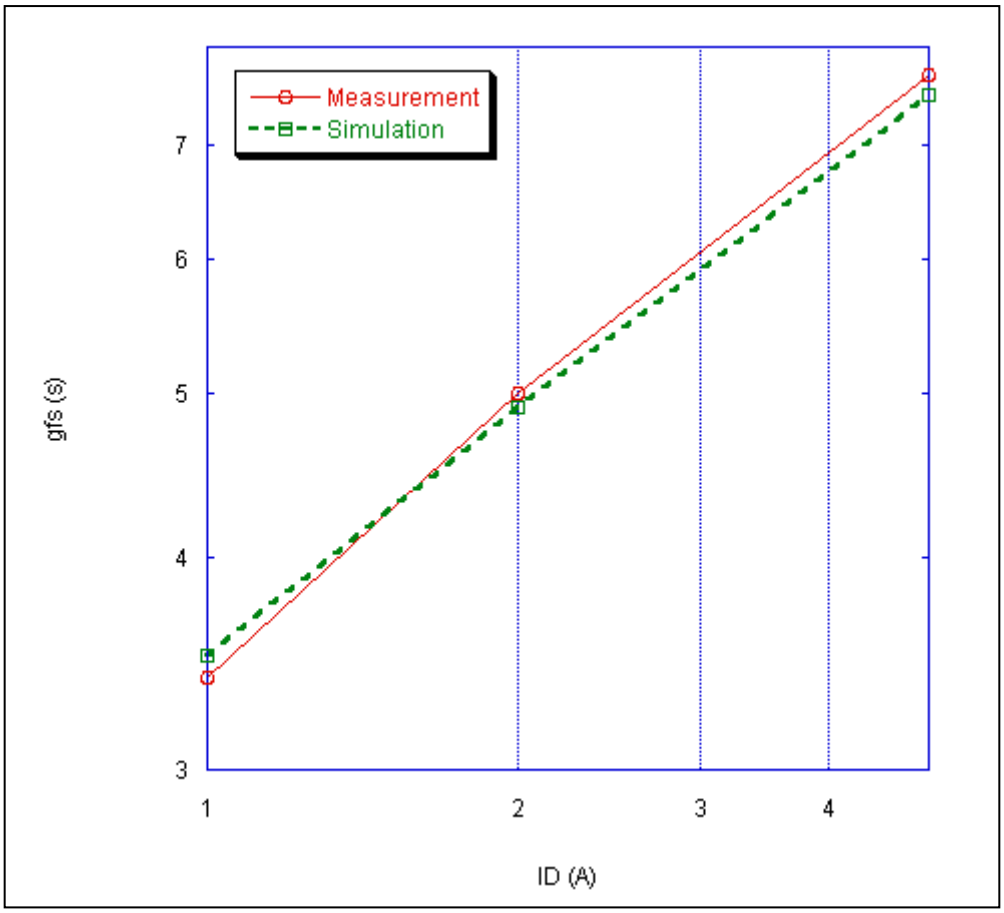
Transconductance Characteristic

Circuit Simulation Result

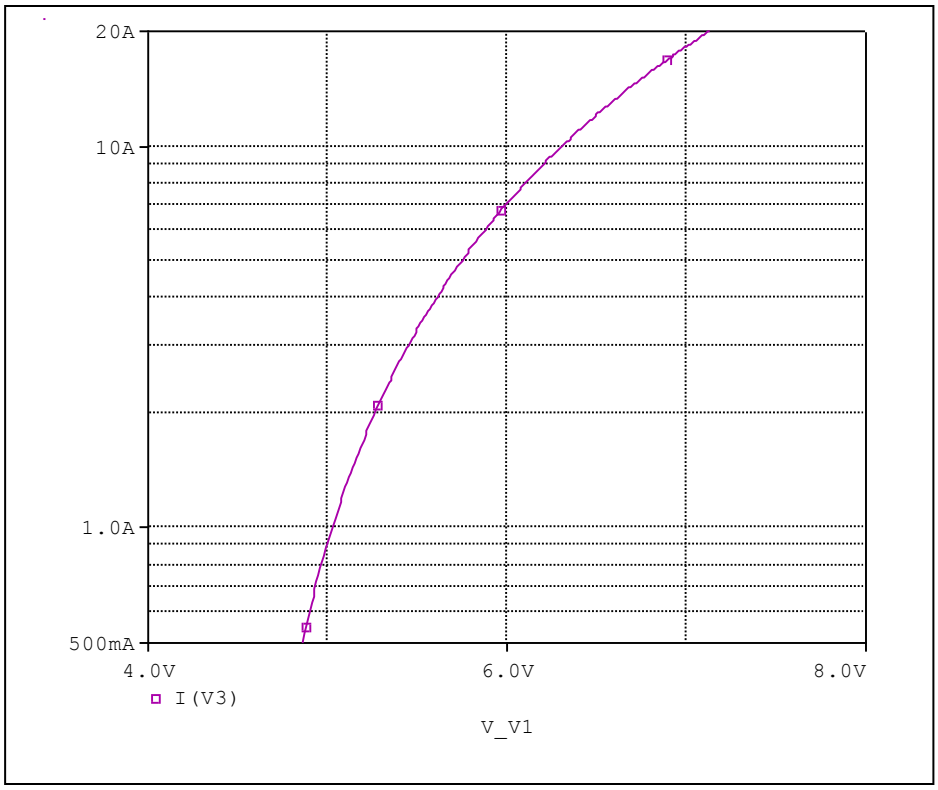


Comparison table

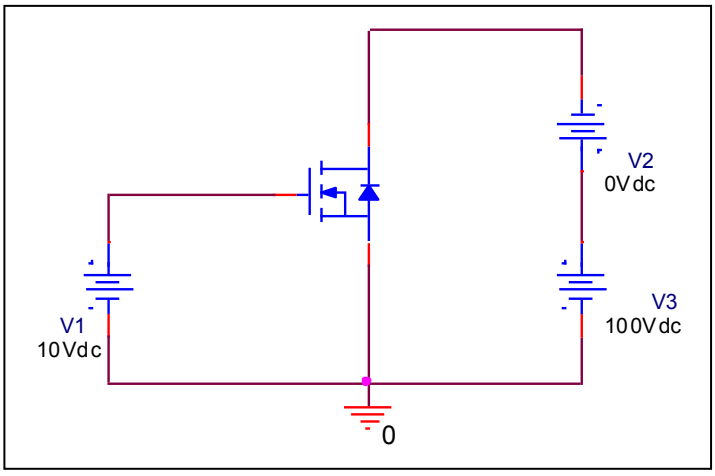
I_D(A)	g_{fs} (S)		Error (%)
	Measurement	Simulation	
1.00	3.40	3.50	2.94
2.00	5.00	4.90	-2.00
5.00	7.70	7.50	-2.60

Vgs-Id Characteristic

Circuit Simulation result

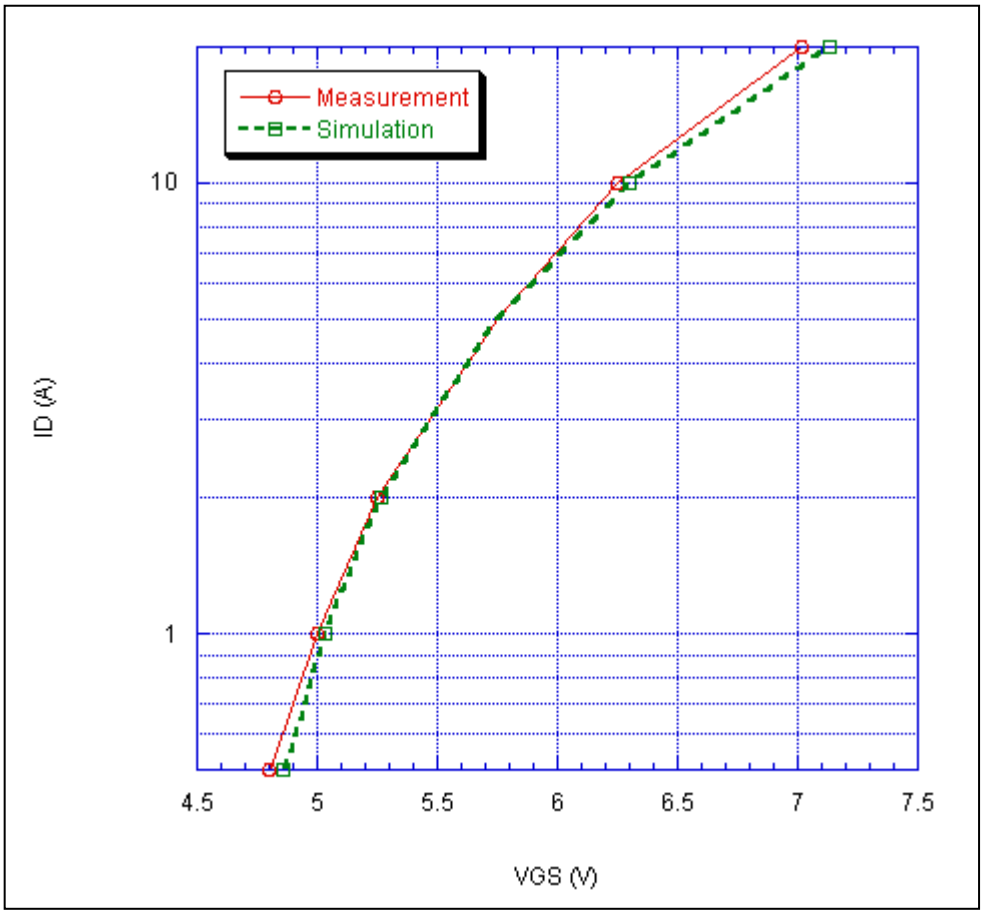


Evaluation circuit



Comparison Graph

Circuit Simulation Result

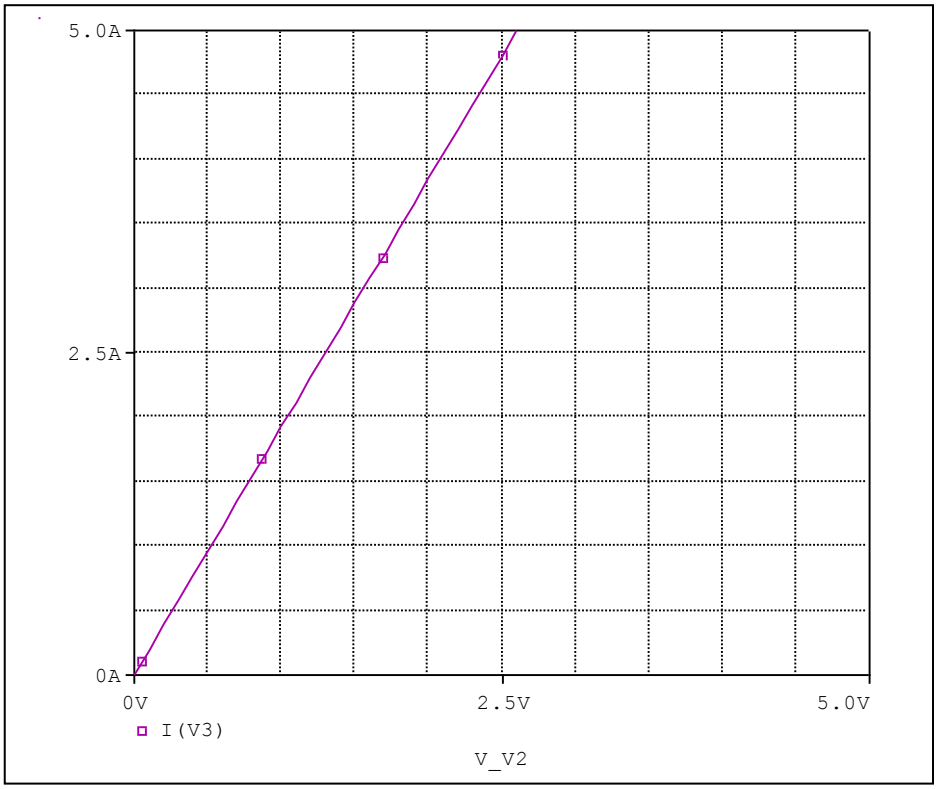


Simulation Result

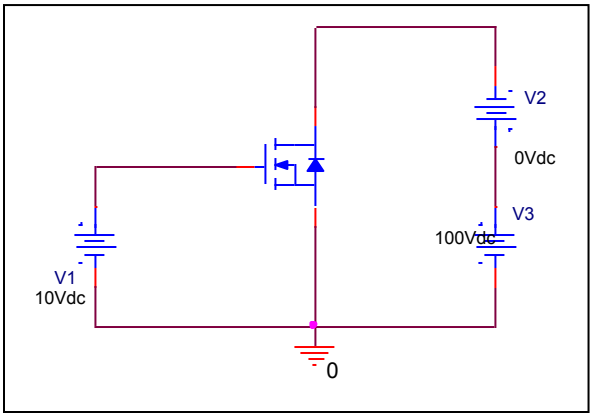
I_D(A)	V_{GS}(V)		Error (%)
	Measurement	Simulation	
0.50	4.80	4.86	1.17
1.00	5.00	5.03	0.55
2.00	5.25	5.27	0.32
5.00	5.75	5.75	-0.06
10.00	6.25	6.30	0.88
20.00	7.02	7.13	1.51

Id-Rds(on) Characteristic

Circuit Simulation result



Evaluation circuit

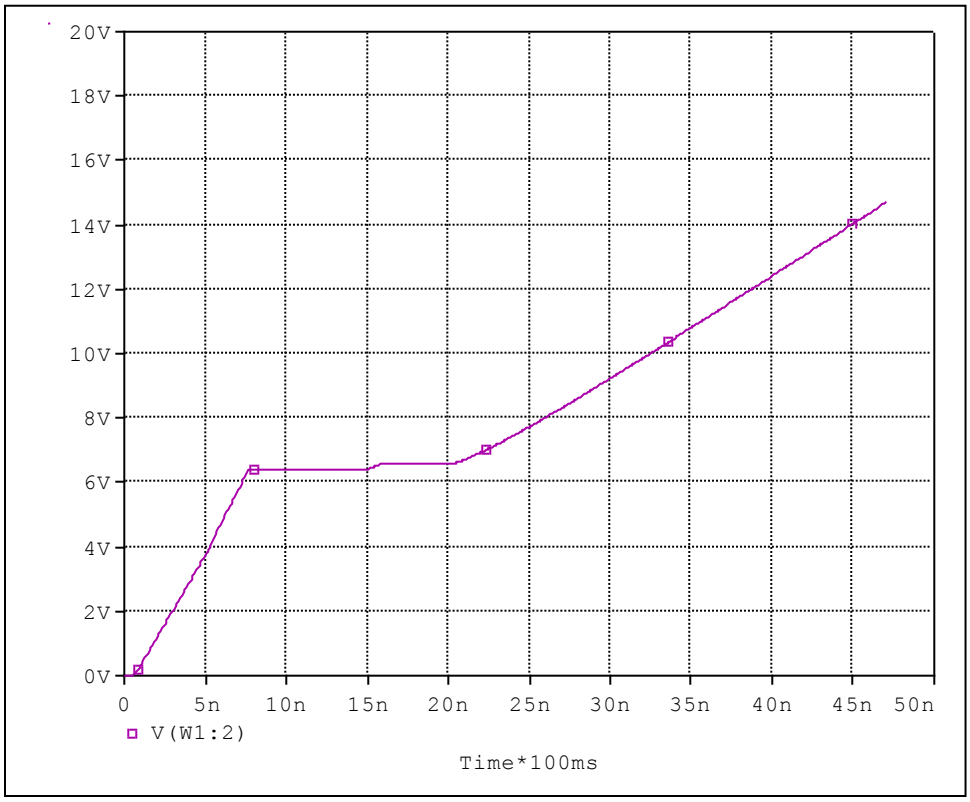


Simulation Result

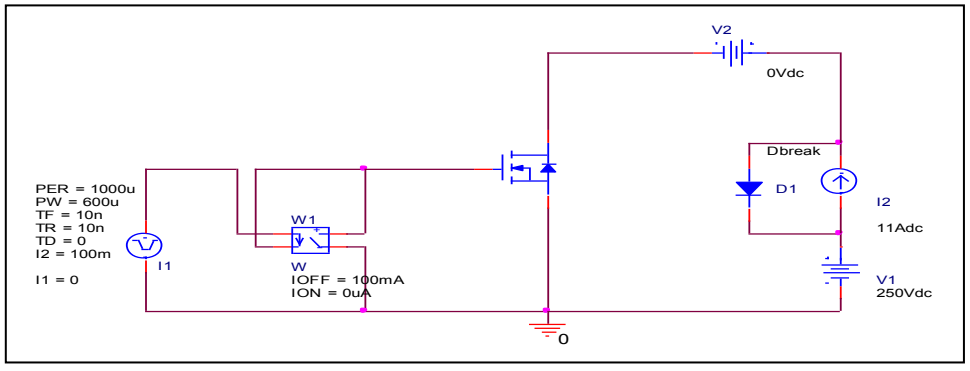
I_D=4.0, V_{GS}=10V	Measurement		Simulation		Error (%)
R_{DS} (on)	0.52	Ω	0.52	Ω	0.00

Gate Charge Characteristic

Circuit Simulation result



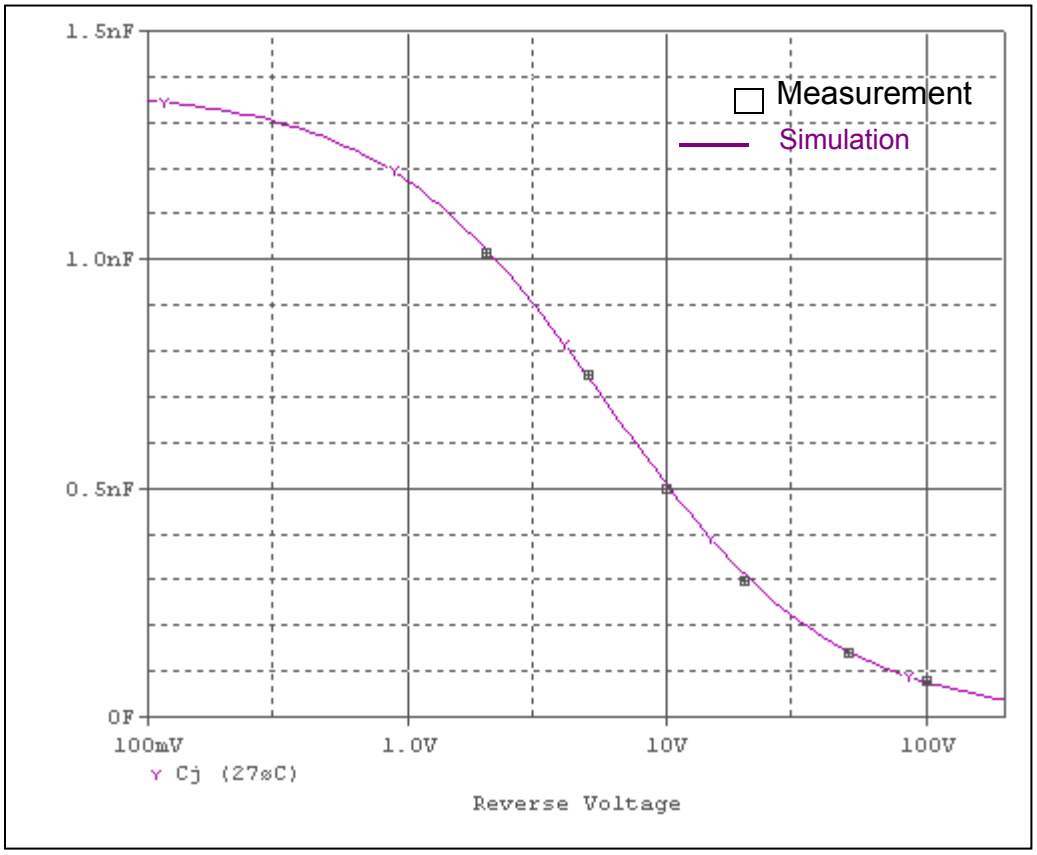
Evaluation circuit



Simulation Result

V_{DD}=250V,I_D=11A	Measurement		Simulation		Error (%)
Qgs	7.70	nC	7.71	nC	0.13
Qgd	13.00	nC	13.05	nC	0.38
Qgd	32.50	nC	32.47	nC	-0.09

Capacitance Characteristic

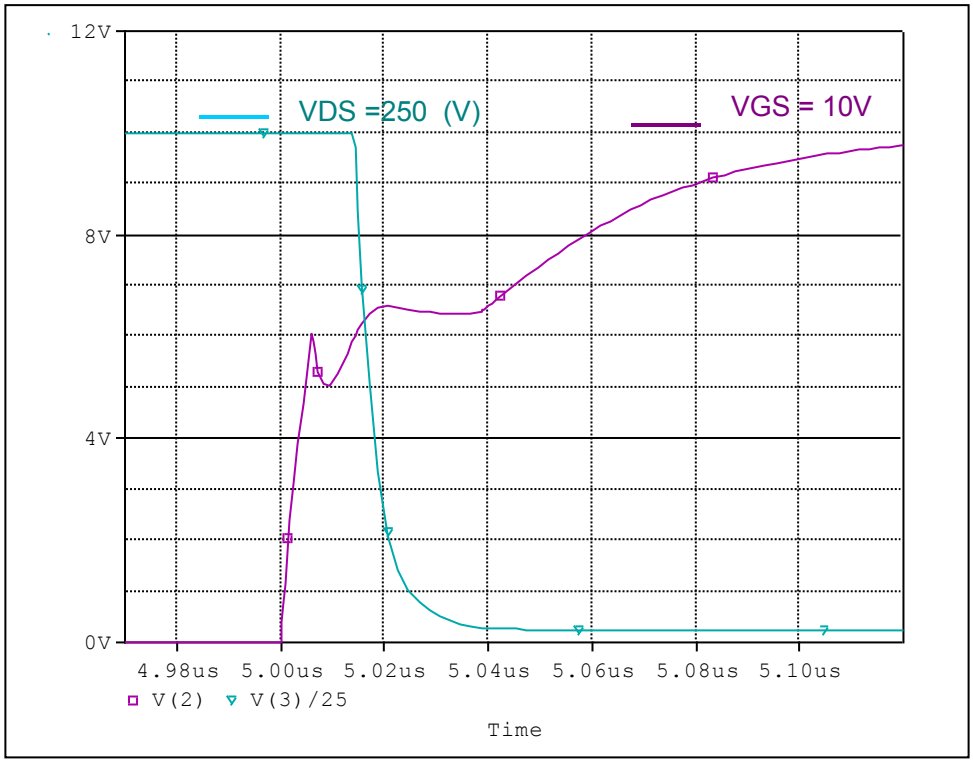


Simulation Result

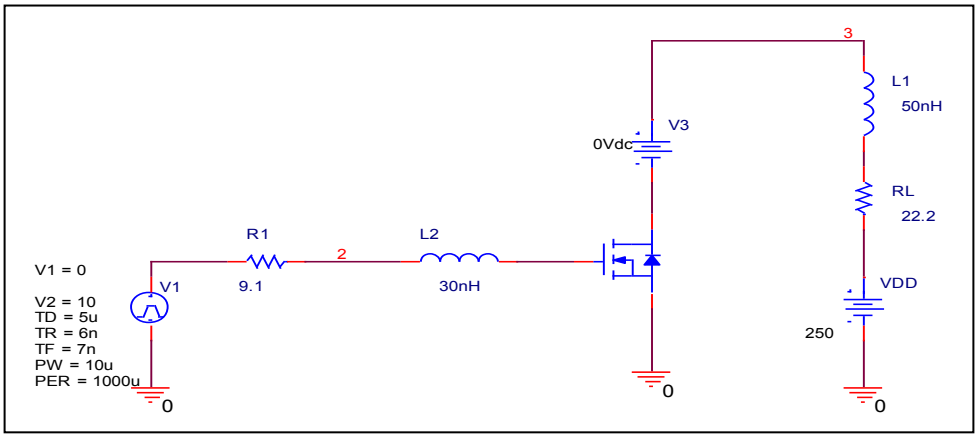
V_{DS}(V)	Cbd(pF)		Error(%)
	Measurement	Simulation	
2.00	1020.00	1024.00	0.39
5.00	750.00	720.00	-4.00
10.00	505.00	507.00	0.40
20.00	300.00	310.00	3.33
50.00	145.00	142.50	-1.72
100.00	83.00	77.00	-7.23

Switching Time Characteristic

Circuit Simulation result



Evaluation circuit

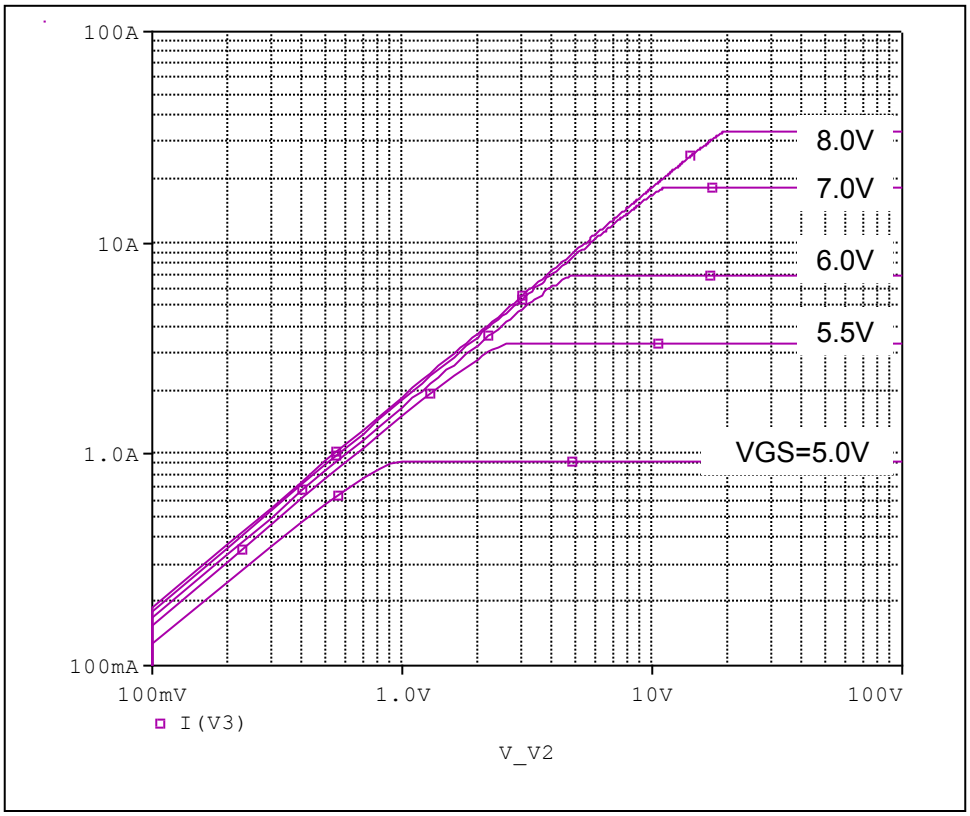


Simulation Result

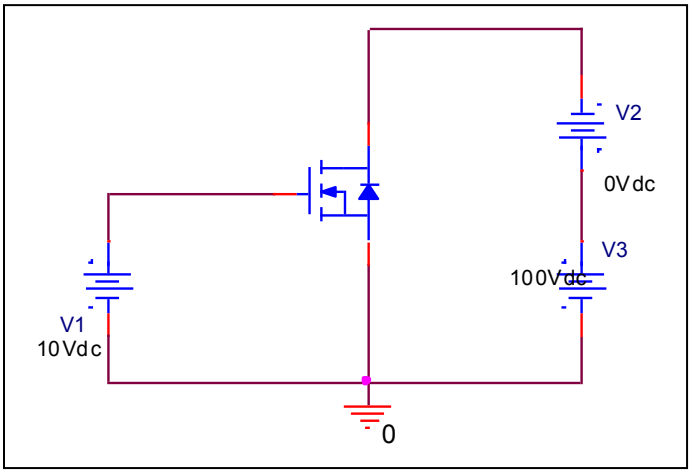
I_D=11A, V_{DD}=250V V_{GS}=0/10V	Measurement		Simulation		Error(%)
td (on)	14.00	ns	14.00	ns	0.00

Output Characteristic

Circuit Simulation result

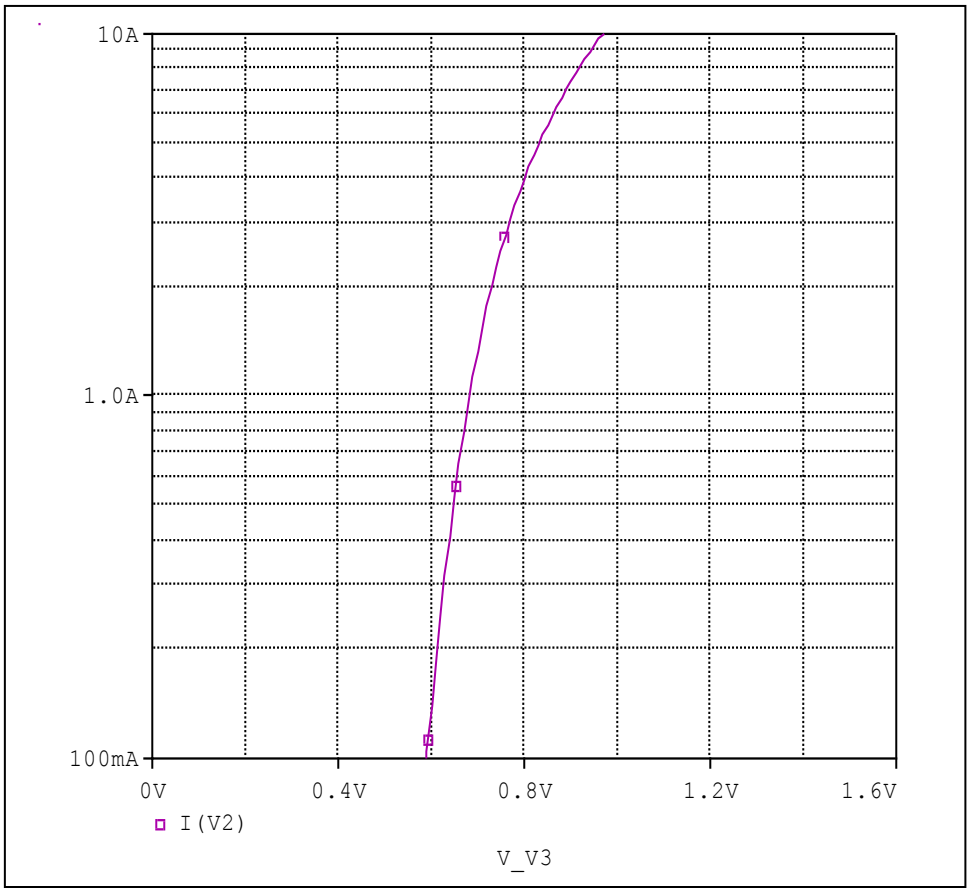


Evaluation circuit

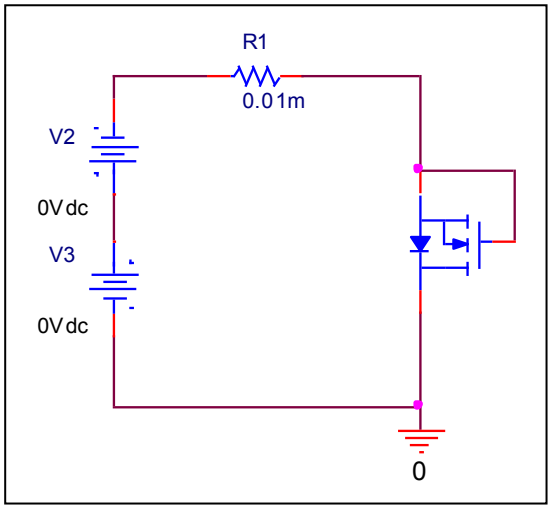


Forward Current Characteristic of Reverse Diode

Circuit Simulation Result

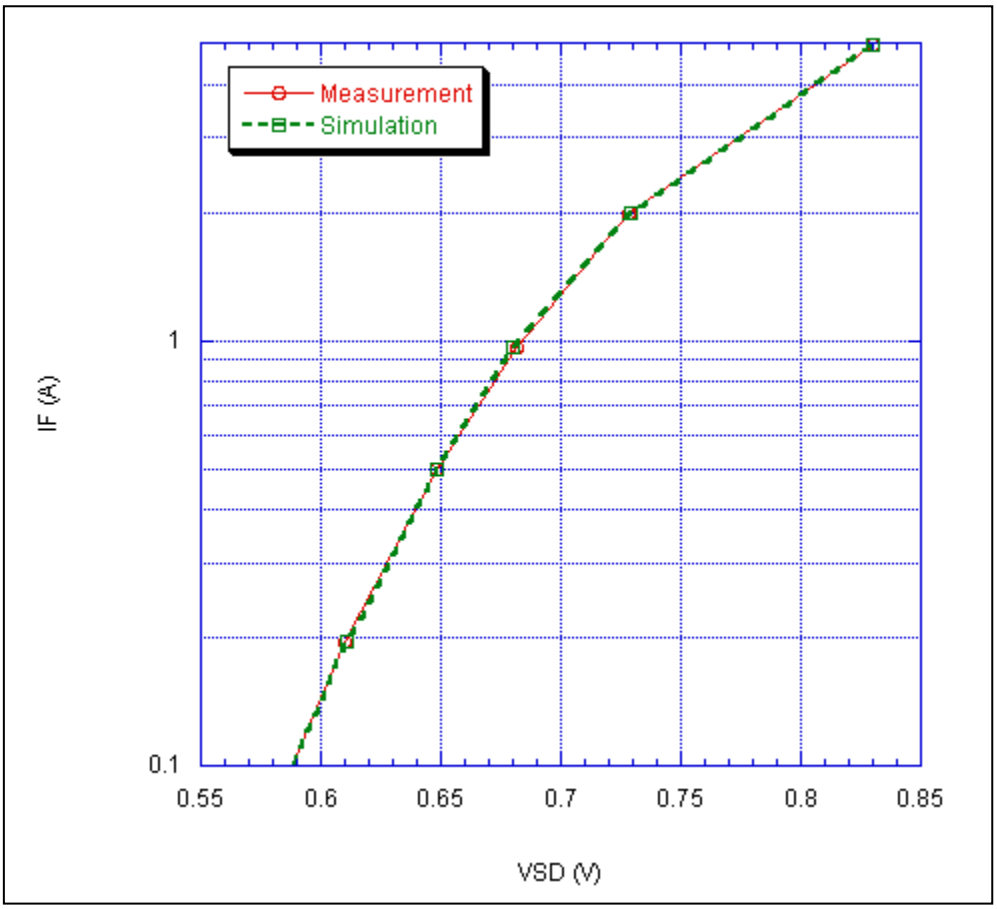


Evaluation Circuit



Comparison Graph

Circuit Simulation Result

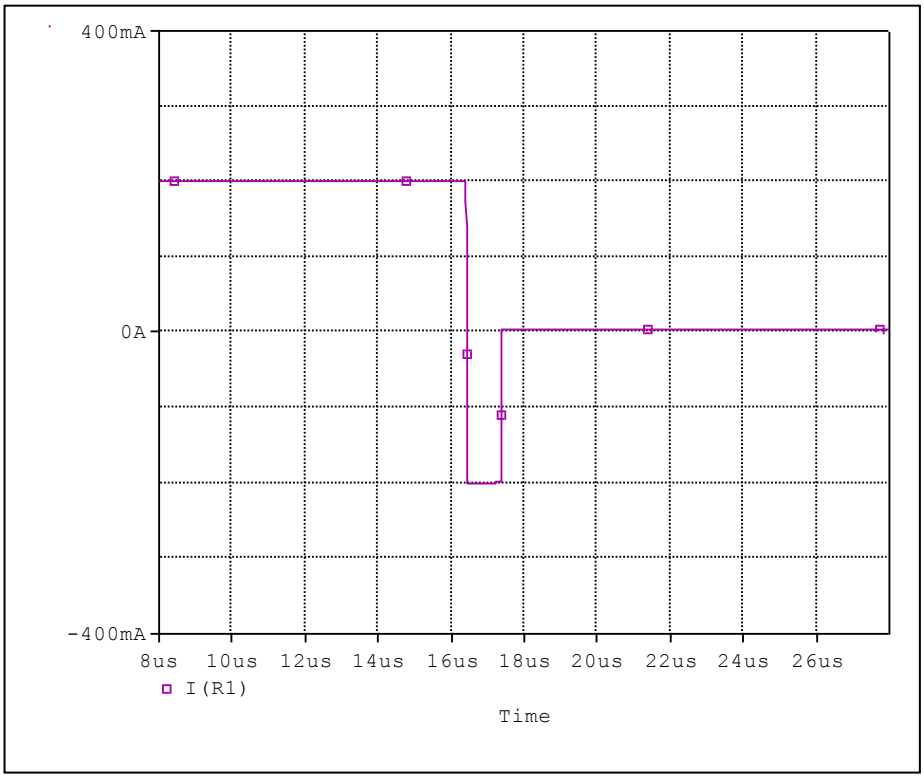


Simulation Result

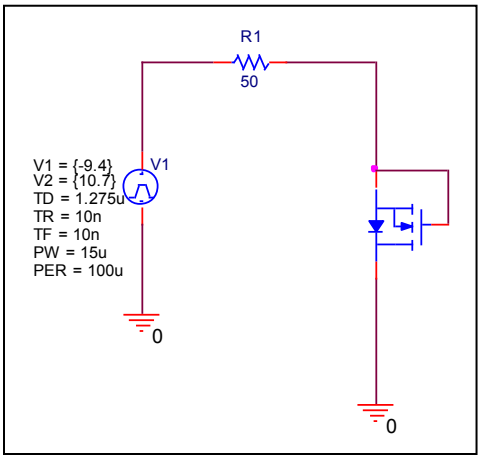
Ifwd(A)	Vfwd(V) Measurement	Vfwd(V) Simulation	%Error
0.085	0.582	0.583	0.196
0.195	0.610	0.611	0.232
0.495	0.648	0.648	-0.033
0.960	0.682	0.680	-0.260
1.985	0.728	0.729	0.139
4.935	0.830	0.830	-0.001

Reverse Recovery Characteristic

Circuit Simulation Result



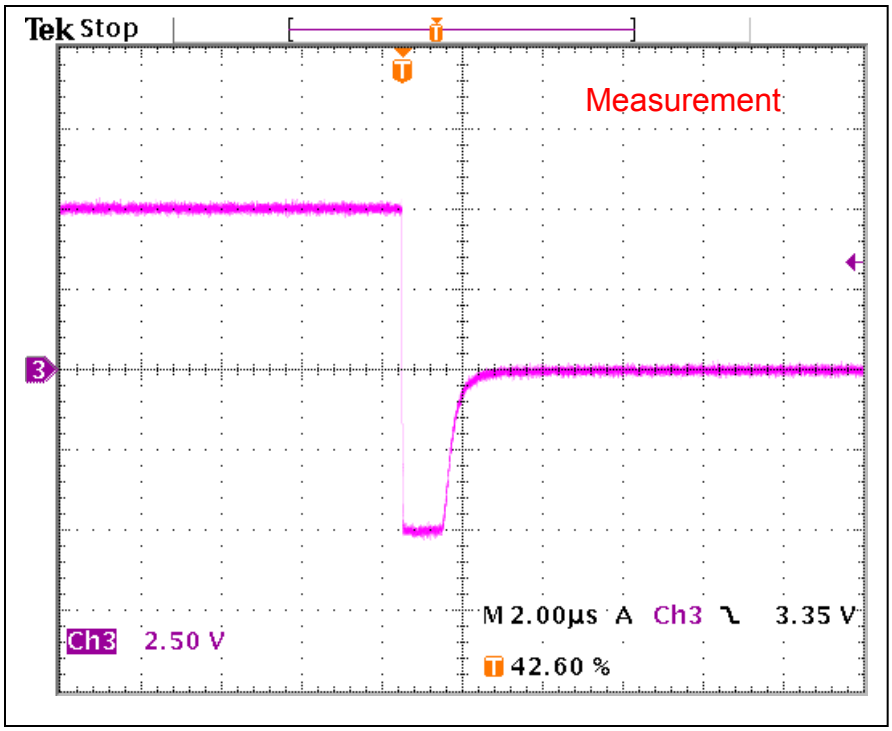
Evaluation Circuit



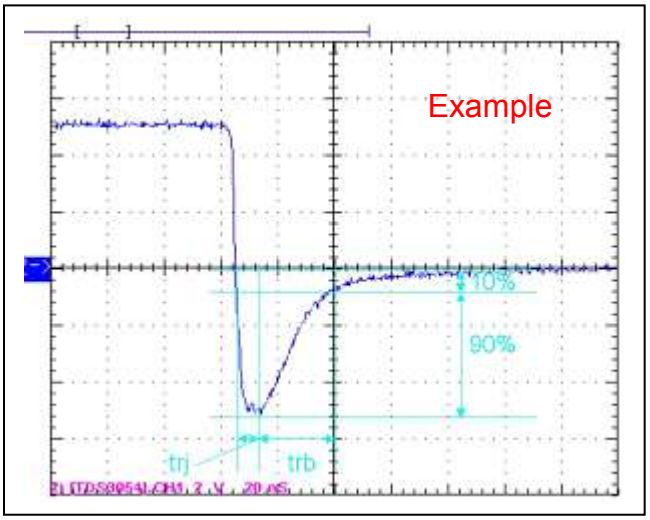
Compare Measurement vs. Simulation

	Measurement		Simulation		Error(%)
Trj	**0.960**	**us**	**0.957**	**us**	**-0.313**

Reverse Recovery Characteristic Reference



trj = 0.96(us)
trb = 0.6(us)
Conditions: Ifwd=Irev=0.2(A), Rl=50



Relation between trj and trb